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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/829,380	04/22/2004	Atsuhiro Hayashi	H-1139	7082
24956	7590 04/11/2006		EXAMINER	
MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.			LAM, TUAN THIEU	
1800 DIAGON SUITE 370	NAL ROAD		ART UNIT	PAPER NUMBER
	A, VA 22314		2816	
			DATE MAILED: 04/11/2000	5

Please find below and/or attached an Office communication concerning this application or proceeding.

5/2

	Application No.	Applicant(s)		
	10/829,380	HAYASHI ET AL.	HAYASHI ET AL.	
Office Action Summary	Examiner	Art Unit		
	Tuan T. Lam	2816	,	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence addre	ss	
A SHORTENED STATUTORY PERIOD FOR RE WHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the m earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNI R 1.136(a). In no event, however, may a riod will apply and will expire SIX (6) MO atute, cause the application to become A	CATION. reply be timely filed NTHS from the mailing date of this commit BANDONED (35 U.S.C. § 133).		
Status				
1)⊠ Responsive to communication(s) filed on 2	7 February 2006.			
<u> </u>	This action is non-final.			
3) Since this application is in condition for allo	wance except for formal mat	ters, prosecution as to the me	erits is	
closed in accordance with the practice und	er <i>Ex parte Quayle</i> , 1935 C.[). 11, 453 O.G. 213.		
Disposition of Claims				
4)⊠ Claim(s) <u>1-13</u> is/are pending in the applicat	ion			
4a) Of the above claim(s) <u>14-27</u> is/are without				
5) Claim(s) is/are allowed.				
6)⊠ Claim(s) <u>1-13</u> is/are rejected.	•.			
7) Claim(s) is/are objected to.				
8) Claim(s) are subject to restriction ar	d/or election requirement.			
Application Papers				
9) The specification is objected to by the Exam	niner			
10) ☐ The drawing(s) filed on 22 April 2004 is/are:		cted to by the Examiner		
Applicant may not request that any objection to	•			
Replacement drawing sheet(s) including the cor	•	•	l.121(d).	
11) The oath or declaration is objected to by the		•		
Priority under 35 U.S.C. § 119				
12)⊠ Acknowledgment is made of a claim for fore	ian priority under 35 U.S.C. (\$ 119(a)-(d) or (f)	-	
a)⊠ All b)□ Some * c)□ None of:	igh phoney andor 00 0.0.0.	<i>y</i> 110(a) (a) 61 (1).		
1 ⊠ Certified copies of the priority docum	ents have been received.			
2. Certified copies of the priority docum		Application No.		
3. Copies of the certified copies of the p			ge	
application from the International Bur	reau (PCT Rule 17.2(a)).			
* See the attached detailed Office action for a	list of the certified copies not	received.		
Attachment(s)				
Notice of References Cited (PTO-892)		Summary (PTO-413)		
 Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB Paper No(s)/Mail Date 	Paper No(s)/Mail Date nformal Patent Application (PTO-152	2)	
5. Patent and Trademark Office				
TOL-326 (Rev. 7-05) Offic	e Action Summary	Part of Paper No./Mail Date 0	4082006	

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DETAILED ACTION

This is a response to the amendment filed 2/27/2006, claims 1-13are under examination. Claims 14-27 have been withdrawn from consideration.

Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Claims 1-13 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, the recitation of "capable of" is not a positive limitation. It is unclear as to if the first and second control means are or are not performing independently of each other.

Therefore, the metes and bounds of the claim can not be determined.

Claims 2-13 are indefinite because of the technical deficiencies of claim 1.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Mooney et al. (USP 6,744,287). Figure 6 of Mooney et al. shows a semiconductor integrated circuit

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comprising an output circuit comprising a plural output MOSFETS (307, 309), a first control means (means to generate En0, En0/, En1, EN1/, EN2, EN2/, EN3, EN3/) that, from among the plural output MOSFETS, selects the number of output MOSFETS to be turned on to control output impedance; and a second control means (means to generate TAP0 to TAP3) that controls a slew rate by controlling a drive signal of the output MOSFETS turned on, wherein the first and second control means are performing independently of each other as called for in claims 1-3, 7-8.

Regarding claim 4, figure 5 shows the predriver buffer as logic gates 306 and 308.

Regarding claims 5-6, figure 6 shows the resistor elements connected in series with each of the output MOSFETs having resistance value greater than the on resistance of the output MOSFETs.

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-4 are rejected under 35 U.S.C. 102(e) as being anticipated by Sine et al. (USP 6,366,867). Figure 2 of Sine et al. shows a semiconductor integrated circuit comprising an output circuit comprising a plural output MOSFETS (252, 254), a first control means (means to generate PRON and NRON) that, from among the plural output MOSFETS, selects the number of output MOSFETS to be turned on to control output impedance; and a second control means (means to generate PSLEW and NSLEW) that controls a slew rate by controlling a drive signal of the output MOSFETS turned on, wherein the first and second control means are performing independently of each other as called for in claims 1-3.

Regarding claim 4, figure 2 shows the predriver buffer as 232 to 232N, 242 to 242N).

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Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 9-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Mooney et al. (USP 6,744,287).

Figure 6 of Mooney et al. shows a semiconductor integrated circuit comprising an output circuit comprising a plural output MOSFETS (307, 309), a first control means (means to generate En0, En0/, En1, EN1/, EN2, EN2/, EN3, EN3/) that, from among the plural output MOSFETS, selects the number of output MOSFETS to be turned on to control output impedance; and a second control means (means to generate TAP0 to TAP3) that controls a slew rate by controlling a drive signal of the output MOSFETS turned on, wherein the first and second control means are performing independently of each other.

The difference seen between Mooney et al. and the present invention is that the Mooney et al. does not show the detailed layout of the output MOSFETs as called for in claims 9-11. However, such layout in an integrated circuit is well known in the art to save space and reduce power consumption. Therefore, outside of non-obvious results, the obviousness of the detailed layout as called for in claims 9-11 will not be patentable under 35USC 103(a).

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Allowable Subject Matter

6. Claims 12-13 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P. CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam Primary Examiner

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4/8/2006